PGPUB-DOCUMENT-NUMBER: 20030065844

PGPUB-FILING-TYPE: nev

DOCUMENT-IDENTIFIER: US 20030065844 A1

TITLE: Method for improving processor performance

PUBLICATION-DATE: April 3, 2003

INVENTOR-INFORMATION:

NAME RULE-47	CITY	STATE	COUNTRY
Lester, Robert A.	Tomball	TX	us
Chin, Kenneth T.	Cypress	TX	US
Blocker, Jim	Cypress	TX	US
Larson, John E.	Houston	TX	US
Jones, Phillip M.	Spring	TX	US
Rawlins, Paul B.	Spring	TX	US

US-CL-CURRENT: 710/107

## ABSTRACT:

Methods for improving processor performance. Specifically, by reducing some of the latency cycles within a host controller, request processing speed can be improved. One technique for improving processing speed involves initiating a deferred reply transaction before the data is available from a memory controller. A second technique involves anticipating the need to transition from a block next request (BNR) state to a bus priority request (BPRI) state, thereby eliminating the need to wait for a request check to determine if the BPRI state must be implemented.

----- KWIC ------

INVENTOR - INNM (5): Jones, Phillip M.

Number   1288   snoop\$3 same coherenc\$3 same memory   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB   2004/03/04   13:40   2004/03/04	
2	
161	
1	
Search\$3 same post\$3 same writeback\$1	
TBM_TDB	
Total	
8 43 future with snoop\$3	
9 (snoop\$3 same coherenc\$3 same memory)	
1 (future with snoop\$3) same (post\$3 near3 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; IBM_TDB USPAT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO;	
11 1 reorder\$3 same (post\$3 near3 writeback\$1)	
DERWENT; IBM_TDB USPAT; US-PGPUB; 14:12 EPO; JPO;	
DERWENT; IBM_TDB USPAT; Same (parallel or simultaneous\$2) US-PGPUB; EPO; JPO;	
DERWENT; IBM TDB USPĀT; Or simultaneous\$2) US-PGPUB; EPO; JPO; DERWENT; DERWENT;	

Page 1